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SERIAL NUMBER 10/632,084	FILING DATE 07/31/2003 RULE	CLASS 714	GROUP ART UNIT 2138	ATTORNEY DOCKET NO. TI-35428 (1962-05407)
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APPLICANTS

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** CONTINUING DATA *****

This appln claims benefit of 60/400,391 07/31/2002

** FOREIGN APPLICATIONS *****

EUROPEAN PATENT OFFICE (EPO) 03291911.0 07/30/2003

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 10/29/2003

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY FRANCE	SHEETS DRAWING 2	TOTAL CLAIMS 25	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <u>8.7.11</u> Initials <u>SS</u>				

ADDRESS

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TITLE

Test and skip processor instruction having at least one register operand

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